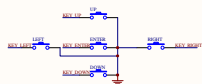
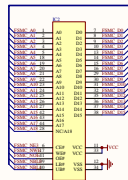
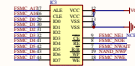
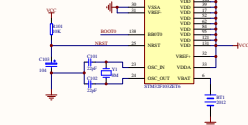
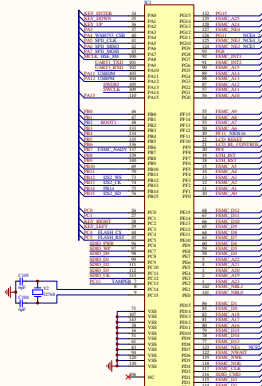
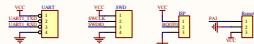
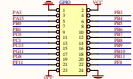
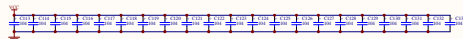
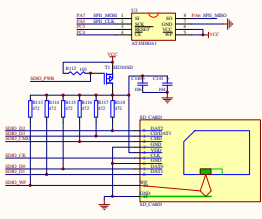


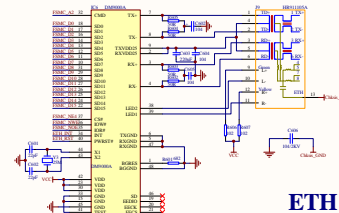
**POWER**



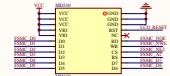
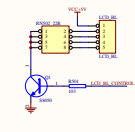
**KEY**



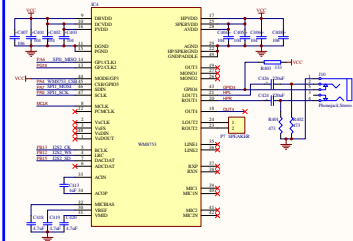
SRAM NE5  
 NAND NE1 NCE2  
 LCD NE3  
 ETH NE4



**ETH**



**LCD**



**CODEC**

Rev	
Date	
Author	
Checked	